

# Stanley Shanfield



## Curriculum Vitae for Stanley Shanfield

### Education:

Massachusetts Institute of Technology, Ph.D., Physics, 1981  
University of California, Irvine, B.S. Cum Laude, Physics, 1977

### Educational Awards:

Outstanding Research Project, U.C. Regents Award, 1977  
Phi Beta Kappa, 1977  
Four Year Scholarship, Tuition & Research, ERDA/DOE, 1977-1981

### Hands-On Professional Expertise:

Semiconductor Physics & Chemistry  
Semiconductor Fabrication: Silicon and III-V Materials  
Semiconductor Device & Integrated Circuit Engineering  
Semiconductor Equipment: Front & Back End  
Semiconductor Packaging Design and Manufacturing  
Passive Component Design and Manufacturing  
RF Design and Fabrication  
Digital Design & Fabrication  
Semiconductor Manufacturing in Silicon and III-V Materials  
MEMS Design and Fabrication: Electrical, Optical, & Sensing Devices  
Fiber Optic Device Design and Fabrication  
Integrated Optics & Electro-Optical Devices  
Plasma Physics

### Specialized Training:

Global Positioning Electronics, Systems and Components  
Six Sigma Semiconductor Manufacturing  
Microwave Engineering  
US Patent Law in Technology  
Patent Application and Prosecution  
Semiconductor Plasma Processing  
Thin Film Physics and Technology  
MEMS-based Gyroscopes and Accelerometers  
Thermoelectric Materials & Technology  
Phased Array Radar Engineering  
Antennas and Electromagnetic Propagation  
Solid State Optical Devices  
Fiber Optic Communication System Operation  
HALT and HAST Reliability Testing Methods

Plasma-Therm LLC  
**Exhibit 2002**  
SPTS Technologies Ltd.  
v. Plasma-Therm LLC  
IPR2017-01314

## Professional Experience:

### Draper Laboratory Cambridge, MA

2003 – present

#### **Division Leader, Advanced Hardware Development Distinguished Member of Technical Staff Technical Director**

Led division (about 80 staff) in re-invigorating multi-chip integrated circuit module facility, more than doubling associated revenues in two years. By most accounts, made division a viable business & technological entity again. Invented & led implementation of an ultra-miniature electronics fabrication technology which became a top laboratory priority. Led team in realization & fabrication of a newly designed precision MEMS-based gyroscope and associated ASIC. Found funding and led team in developing a miniature power source with energy density at least two orders of magnitude higher than any source previously built. Developed fabrication technology for semiconductor-based low phase noise oscillator design, allowing for receiver operation with extremely low signal strength. Many awards received, most recently, the Draper 2010 Distinguished Performance Award, and 2010 Best Patent Award.

### Clarendon Photonics Newton, MA

2001 – 2003

#### **Director, Packaging & Integration**

30 person photonic chip startup with \$18 million 2nd round funding. Invented and productized new, low cost and reliable semiconductor processing, packaging and pig-tailing technology for optical add-drop multiplexer. Established assembly and packaging process, and developed control electronics. Partner with Micron Technologies, using their R&D semiconductor facility.

### AXSUN Technologies Bedford/Billerica, MA

1999 – 2001

#### **Vice President, Operations 1999 – 2000**

Initially three staff members with \$6 million funding. Designed, fabricated and productized AXSUN's micro-electromechanical (MEM) Fabry-Perot optical filter. Patents granted on semiconductor processing and control electronics. Completed facility and semiconductor processing design, then completely equipped. Raised 2nd round funding for \$36 million. Established process and fabrication facility in Belfast, Northern Ireland for producing thick oxide silicon-on-insulator material.

#### **Director, Manufacturing & Wafer Fab Technology**

**2000 – 2001**

After 3rd round funding, led device manufacturing, creating wafer fab and assembly infrastructure; hired 70+ people, led production. Delivered first generation product for revenue to multiple customers. Converted pure technology to dominant company revenue with high yield.

Company purchased by Volcano Technologies, San Diego.

**Raytheon Corporation Lexington/Andover, MA**

**1985 - 1999**

**Manager, Semiconductor Operations**

**1996 – 1999**

Built and led a 300 employee, \$60 million revenue 24/7 semiconductor development and manufacturing operation resulting from the consolidation of a number of smaller organizations. Key player in technological development and recipient of Raytheon's 6 Sigma Leadership training. Decision maker in Texas Instrument group acquisition, providing significant expert opinion on semiconductor and design facilities. Obtained state-of-art yields using best available steppers, deep reactive ion etching, plasma assisted CVD, and ion-implantation equipment, and disciplined design-for steppers, deep reactive ion etching, plasma assisted CVD, and ion-implantation equipment, and disciplined design-for-manufacturing circuit design and layout methodology.

**Research Laboratory Manager**

**1992 – 1996**

Leader of a 90 employee development and contract research organization in high performance semiconductor devices and circuits, measurement, assembly and wafer fab. Led a team which invented and implemented a major revenue generating technology (\$.100 million) based on semiconductor device development (pseudomorphic high electron mobility transistor). Increased outside research funding by 50% in 3 years through superior technical performance relative to competitors.

**Section Manager, Semiconductors & ICs**

**1985 – 1992**

Led a MM-Wave Circuit and Module Development program over 2 years, leading to production win of a satellite terminal electronics generating \$320 million in sales. Developed processes for fabricating high power, high frequency multi-function integrated circuits, and combining high performance digital and analog devices in a single integrated circuit.

**Spire Corporation Bedford, MA**

**1981 – 1984**

**Staff Scientist  
Senior Staff Scientist**

Developed new methods for low temperature deposition of plasma-assisted CVD epitaxial silicon. Wrote joint papers with MIT professor, and had process adopted by equipment manufacturers. Built, operated and characterized ion-assisted deposition system for making coating for semiconductor and machine tool industries. Process eventually purchased by Kennametal, Inc.

**Publications**

**Restricted Publications & Reports**

**Process Sequence for Formation of Ultra-High Density Multi-Chip Modules:**

A high yield, low cost method for creating a system-in-a-package consisting of numerous semiconductor die, passive components, and sensors. Process formed basis for new (2008) facility.

**Design and Method of Fabrication of Ultra-High Density Radioisotope Power Source:**

In test, a miniature power source that achieves energy density more than 1000X the best chemical battery. Method uses planar semiconductor processing of bulk thermoelectric materials.

**Design and Method of Achieving Extremely Low Crystal Oscillator Phase Noise:**

Method developed for very low power refrigeration of quartz or sapphire crystal resonators, resulting in extremely low phase noise oscillators. The low phase noise allows extremely high sensitivity in digital receivers, including GPS receivers, leading to use in extremely low signal conditions.

**Design, Evaluation & Production of MEMS-based Fabry-Perot Interferometer:**

Design and method for ultra-compact spectral analyzer made using semiconductor and optical thin film processing.

**Design & Evaluation of Ultra-Fast Control Electronics for Integrated Optical Multiplexer:**

Design and performance evaluation of silicon-based integrated optical multiplexer using chip-based local heating

**Design and Fabrication of Q-band MILSTAR Communications Terminal Transmitter:**

Record power and efficiency 44 GHz transmitter design using new transistor design, and combined waveguide

**Key Publications (selected)**

**Process Characterization of PSG and BPSG Plasma Deposition**, J. Electrochem. Soc., Volume 131, Issue 9, pp. 2202-2203

**A double-recessed Al<sub>0.24</sub>GaAs/In<sub>0.16</sub>GaAs pseudomorphic HEMT for Ka- and Q-band power Applications**, Electron Device Letters, IEEE, Volume 14, Issue 9, pp. 456 - 458

**Formation of Thick Metal Structures on GaAs MMICs Using Image Reversal Lithography and Evaporated Metal Deposition**, J. Electrochem. Soc., Volume 136, Issue 9, pp. 2687-2690

**Contact Hole Etching in Load-Locked Hexagonal Reactive Ion Etch System**, J. Electrochem. Soc., Vol. 131, No. 8, 1984

**An AlGaAs/InGaAs pseudomorphic high electron mobility transistor with Improved Breakdown Voltage for X and Ku-band power applications**, Microwave Theory and Techniques, IEEE Transactions on, Volume 41, Issue 5, May 1993, pp. 752 - 759

**Hot-electron-induced Degradation of Metal-Semiconductor Field-Effect Transistors**, Integrated Circuit Symposium, 1994. Technical Digest 1994., 16th Annual Volume, Issue, 16-19 Oct. 1994, pp. 259

**Ion Beam Deposition of Cubic Boron Nitride**, J. Vac. Sci. Technol. A Volume 1, Issue 2, pp. 323-325

**Patents****US Patent 6504235 - Passivation layer and process for semiconductor devices**

Method of coating semiconductor devices that prevented parametric shift in electrical performance. Solved key processing problem.

**US Patent 4440108 - Ion Beam Deposition Apparatus**

Design of equipment for deposition of thin films in the presence of ion bombardment. System produced thin films of interest for mechanical, electrical and optical properties and was sold as an equipment product.

**US Patent 6525880 - Integrated Tunable Fabry-Perot filter and Method of Making Same**

Design and method for fabricating very small, very high performance variable optical filter using semiconductor fabrication technology. In current use in fiber optical networks, chemical sensors, and 3-D medical imaging applications.

**US Patent 5175020 - Boron Nitride Films and Process of Making Same**

Ion assisted deposition of ultra-hard cubic boron nitride films for semiconductor and machine tool applications. Significant use in both areas.

**US Patent 4526673 - Coating Method**

Method for deposition of thin films used in semiconductor device fabrication. Method based on direct control of the kinetics of thin film deposition.

**US Patent 7727806 - Systems and Methods for High Density Multi-Component Modules**

Method for fabrication of electronic modules using multiple thinned integrated circuits, patterned multi-level interconnects, passive electronic components, and sensors

**US Patent Application 2009/TBD - Devices, systems, and methods for controlling the temperature of resonant elements**

Devices and systems for achieving low phase noise crystal oscillators using unique low power thermoelectric structures

**Expert Witness Experience:**

**See attachment**

**Contact information for Stanley Shanfield**

**STANLEY SHANFIELD**

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Newton, MA 02465

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617-258-3392 (office)

617-699-7262 (mobile)

**Email:** shanfield@alum.mit.edu

## Expert Witness Experience: Dr. Stanley Shanfield

**Case:** Taiwan Semiconductor Manufacturing Company, Ltd. (Petitioner) vs. Godo Kaisha IP Bridge 1 (Patent Owner), Inter Partes Review, Patent US 7,893,501, and Patent US 7,417,289; Case: -

**Clients:** Wilmer Hale and Haynes Boone, for TSMC

**Working Period:** Q2, 2016 – Q2 2017

**Technology at Issue:** Semiconductor device and process design

**Outcome:** IPR under review

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**Case:** Vizio (Petitioner) v. Nichia, Inter Partes Review, Patent US 8530250, Case: -

**Clients:** Ropes and Gray, for Vizio

**Working Period:** Q3, 2016 – Q2 2017

**Technology at Issue:** LED packaging design and manufacturing

**Outcome:** IPR under review

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**Case:** Samsung Electronics (Petitioner) vs. Flamm, Inter Partes Review Patent: RE 40,264 E, Case: IPR2015-01330

**Client:** Arnold and Porter, LLP for Samsung

**Working Period:** Q1, 2016 – Q3 2016

**Technology at Issue:** Semiconductor plasma etching

**Outcome:** IPR under review

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**Case:** Samsung Electronics (Petitioner) vs. Lexington Luminance LLC, Inter Partes Review Patent: 6,936,851 Case: -

**Client:** Arnold and Porter, LLP for Samsung

**Working Period:** Q4, 2015 – Q1 2016

**Technology at Issue:** Semiconductor material growth

**Outcome:** IPR under review

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**Case:** American Technical Ceramics (ATC) vs. Presidio Components, Inter Partes Review - Patent: 6,337,791, Case: IPR2015-01330; Patent: 6,992,879, Case: IPR2015-01331; Patent: 6,144,547, Case: IPR2015-01332;

**Client:** Mintz-Levin... & Nutter McClennnen... for American Technical Ceramics

**Working Period:** Q4, 2015 – Q3 2016

**Technology at Issue:** High Frequency Surface Mount Ceramic Capacitors

**Notable Detail:** Combined issues of high frequency performance and manufacturing process;

**Outcome:** Trial scheduled

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**Case:** Progressive Semiconductor Solutions v. Broadcom Corp., Inter Partes Review, Infringement, Case: 8:15-cv-02076-DFM

**Client:** O'Melveny and Myers, LLP, for Broadcom

**Working Period:** Q3, 2016 – Q4 2016

**Technology at Issue:** Integrated circuit design

**Outcome:** Postponed

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**Case:** Progressive Semiconductor vs. Qualcomm (Petitioner), Inter Partes Review Patent: 6,862,208, Case: IPR 2014 - 01504

**Client:** Knobbe Martens Olson & Bear, for Qualcomm

**Working Period:** Q3, 2014 – Q1 2015

**Technology at Issue:** Integrated Circuit (Memory) Electrical Design

**Notable Detail:** Electrically complex circuit design issue in multiple patents, suited to IPR

**Outcome:** Final decision in favor of Qualcomm

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**Case:** Bluestone Innovations, LLC vs. LG Electronics (“Vizio”), Case No. 3:13-cv-01770 (N.D. Cal)

**Client:** Glaser, Weil, Fink, Jacobs, for LG Electronics

**Working Period:** Q3, 2013 – Q4 2014

**Technology at Issue:** Semiconductor Material Growth Process and Device Design

**Notable Detail:** Required procurement and preparation of demonstration models

**Outcome:** Settled

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**Case:** Intel Corporation vs. X2Y, Inc., ITC Investigation No. 337-TA-781, Patent Infringement; claimed damages in hundreds of millions of dollars

**Client:** WilmerHale, for Intel

**Working Period:** Q4, 2011 – Q1-Q2 2012

**Technology at Issue:** Integrated Circuit Fabrication Processes, Layout and Electrical Design

**Notable Detail:** Shanfield testified at ITC

**Outcome:** Intel prevailed on all infringement counts; Administrative Law Judge quoted Shanfield expert testimony multiple times supporting his conclusions

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**Case:** Solannex, Inc. vs. Miasole, Inc., Northern District of California, Case: CV11 0171 EDL, Patent Infringement;

**Client:** Quinn Emanuel Urquhart & Sullivan, for Solannex

**Working Period:** Q3-Q4, 2011

**Technology at Issue:** Solar Cell Fabrication Process, Layout, and Mechanical Packaging

**Outcome:** Information not released

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**Case:** Spansion LLC vs. Samsung Electronics, Eastern District of Virginia, Civil Action No. 1:10CV881, Patent Infringement; Countersuit, Samsung vs. Spansion LLC; claimed damages in hundreds of millions of dollars

**Client:** Fish and Richardson, for Samsung

**Working Period:** Q4, 2010, Q1-Q2, 2011

**Technology at Issue:** Integrated Circuit Fabrication Processes, Layout and Electrical Design

**Notable Detail:** Shanfield managed all reverse engineering work, performed on-site layout inspection; this background work was important in reaching settlement prior to trial

**Outcome:** Samsung prevailed on all infringement counts; Samsung successful with one countersuit infringement claim with more than \$100 million damages

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**Case:** Quimonda A.G. vs. Seagate Technology, LSI Corporation, ITC Investigation No. 337-TA-665, Patent Infringement; claimed damages in hundreds of millions of dollars

**Client:** Steptoe and Johnson, for Seagate and LSI

**Working Period:** Q1-Q3, 2009

**Notable Detail:** Shanfield testified at ITC

**Technology at Issue:** Integrated Circuit Fabrication Processes, Layout and Electrical Design

**Outcome:** Seagate and LSI prevailed on all infringement counts; ITC decision affirmed in US Federal Court